

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) An integrated apparatus comprising:
a high-level-processor; and
a low-level-processor;
said integrated apparatus to provide a first apparatus operated with said high-level-processor in a first mode of operation operating high-level application software, and said integrated apparatus to provide a second apparatus operated with said low-level-processor in a second mode of operation operating low-level application software.
2. (Previously Presented) An integrated apparatus as claimed in claim 1, said low-level-processor to be powered in said second mode of operation to provide said second apparatus, while said high-level-processor remains at least substantially unpowered in said second mode of operation.
3. (Previously Presented) An integrated apparatus as claimed in claim 1, said integrated apparatus to allow at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.
4. (Previously Presented) An integrated apparatus as claimed in claim 1, said low-level-processor to operate in a first manner in the first apparatus mode, and to operate in a differing manner in the second apparatus mode.
5. (Original) An integrated apparatus as claimed in claim 1, said low-level-processor being provided as part of a chipset providing other components of said integrated apparatus.

6. (Original) An integrated apparatus as claimed in claim 1, wherein said first apparatus is a portable computer.

7. (Currently Amended) An integrated apparatus comprising:
high-level-processor means for providing high-level-processing operating high-level application software as a portable computer; and
low-level-processor means for providing low-level-processing operating high-level application software as a personal organizer;

said integrated apparatus to provide a first apparatus operated with said high-level-processor means in a first mode of operation, and said integrated apparatus to provide a second apparatus operated with said low-level-processor means in a second mode of operation.

8. (Previously Presented) An integrated apparatus as claimed in claim 7, said low-level-processor means to be powered in said second mode of operation to provide said second apparatus, while said high-level-processor means remains at least substantially unpowered in said second mode of operation.

9. (Previously Presented) An integrated apparatus as claimed in claim 7, said integrated apparatus to allow at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.

10. (Previously Presented) An integrated apparatus as claimed in claim 7, said low-level-processor means for operating in a first manner in the first apparatus mode, and for operating in a differing manner in the second apparatus mode.

11. (Original) An integrated apparatus as claimed in claim 7, said low-level-processor means being provided as part of a chipset providing other components of said integrated apparatus.

12. (Original) An integrated apparatus as claimed in claim 7, wherein said first apparatus is a portable computer.

13. (Currently Amended) A method of providing an integrated apparatus comprising: providing a high-level-processor adapted to provide high-level-processing of a portable computer; and

providing a low-level-processor adapted to provide low-level-processing of a personal organizer;

providing a first apparatus by operation of said high-level-processor in a first mode of operation; and

providing a second apparatus by operation of said low-level-processor in a second mode of operation.

14. (Original) A method as claimed in claim 13, comprising: powering said low-level-processor in said second mode of operation to provide said second apparatus, while at least substantially non-powering said high-level-processor in said second mode of operation.

15. (Original) A method as claimed in claim 13, comprising at least one of: powering into said first mode of operation during times exclusive of powering into said second mode of operation, and powering into said second mode of operation during times exclusive of powering into said first mode of operation.

16. (Original) A method as claimed in claim 13, comprising: operating said low-level-processor in a first manner in the first apparatus mode; and operating said low-level-processor in a differing manner in the second apparatus mode.

17. (Original) A method as claimed in claim 13, comprising providing said low-level-processor as part of a chipset providing other components of said integrated apparatus.

18. (Original) A method as claimed in claim 13, comprising providing said first apparatus as a portable computer.

19. (Currently Amended) A portable computer system adapted to be selectable into a differing apparatus, comprising:

a high-level-processor to provide high-level-processing for operation as a portable computer; and

a low-level-processor to provide low-level-processing for operation as a personal organizer;

said portable computer system to provide a portable computer apparatus operated with said high-level-processor processing operating high-level application software in a portable computer mode of operation processing, and said portable computer system to provide said differing apparatus operated with said low-level-processor processing operating low-level application software in another mode of operation.

20. (Currently Amended) A portable computer system as claimed in claim 19, said portable computer system ~~[[being]]~~ to allow at least one of: powering into said portable computer mode of operation during times exclusive of powering into said another mode of operation, and powering into said another mode of operation during times exclusive of powering into said portable computer mode of operation.

21. (Currently Amended) An integrated apparatus comprising:

a high-level processor to operate in a first mode of operation to provide a first apparatus processing high-level application software as a portable computer; and

a low-level processor to operate in a second mode of operation to provide a second apparatus processing operating low-level application software as a personal organizer.